



Substitute for form 1449A/PTC

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	2	Examiner Name	Chi Ho A. Lee
				Attorney Docket Number	81862P146

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
~		US-6,104,721	08-15-2000	Hsu	
		US-5,748,468	05-03-1998	Notenboom, et al.	
		US-5,367,678	11-22-1994	Lee, et al.	
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		US-5,787,149	07-28-1998	Yousefi, et al.	
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		US-6,002,689	12-14-1999	Christie, et al.	
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		US-4,042,958	08-16-1977	Saylor, et al.	
~		US-RE. 31,863	04-09-1985	Saylor, et al.	
		US-			
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	10/1/03
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Sheet 2 of 2

Complete if Known

Application Number	09/270,297
Filing Date	March 15, 1999
First Named Inventor	Satish D. Deo
Art Unit	2663
Examiner Name	Chi Ho A. Lee
Attorney Docket Number	81862P146

OTHER ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>N</i>		"Motorola Announces New, Higher Performance Versions Of Its Popular MPC860 PowerQUICCTM Microprocessors" Press Release, April 28, 1999 , Website, http://apspg.motorola.com/press/press/042999/powerquicc.html , 3 pages.	
		MOTOROLA "MPC860 PowerQUICC Technical Summary", MPC860TS/D, Rev. 0.1, Sections 1.1 - 1.8 , November 2001, pages 1-9.	
		IBM Technical Disclosure Bulletin, "Task Management Of Multiple Digital Signal Processors" Vol. 34, No. 7B, December 1991, pages 237 - 239.	

Examiner Signature	Date Considered
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